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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/773,008	02/05/2004	Robert William Dobbs	200209624-1 5358	
22879	7590 11/13/2006	EXAMINER		
	PACKARD COMPAN	DEBERADINIS, ROBERT L		
P O BOX 272400, 3404 E. HARMONY ROAD INTELLECTUAL PROPERTY ADMINISTRATION FORT COLLINS, CO 80527-2400			ART UNIT	PAPER NUMBER
			2836	

DATE MAILED: 11/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applica	ion No.	Applicant(s)	· · · · · · · · · · · · · · · · · · ·			
Office Action Summary		10/773,	008	DOBBS ET AL.				
		Examin	er	Art Unit				
		Robert [)eBeradinis	2836				
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).								
Status								
1)⊠	Responsive to communication(s) filed or	n 02 August 200	95.					
2a)[
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Dispositi	ion of Claims							
4)⊠	4) Claim(s) <u>1-25</u> is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
5)🖂	5)⊠ Claim(s) <u>1-23 and 25</u> is/are allowed.							
6)⊠	Claim(s) <u>24</u> is/are rejected.							
7)	Claim(s) is/are objected to.							
8)□	Claim(s) are subject to restriction	and/or election	requirement.					
Applicati	ion Papers			·				
9)[The specification is objected to by the Ex	aminer.	•					
10)	The drawing(s) filed on is/are: a)[accepted or b	o) objected to by the	Examiner.				
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority ι	ınder 35 U.S.C. § 119							
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:								
	1. Certified copies of the priority documents have been received.							
	2. Certified copies of the priority documents have been received in Application No							
	3. Copies of the certified copies of the priority documents have been received in this National Stage							
application from the International Bureau (PCT Rule 17.2(a)).								
* See the attached detailed Office action for a list of the certified copies not received.								
	w.s							
Attachmen	t(s) e of References Cited (PTO-892)		4) Interview Summary	(PTO-413)				
	e of References Ched (P10-692) e of Draftsperson's Patent Drawing Review (PTO-9	48)	Paper No(s)/Mail Da	ate				
3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date 8/2/05. 5) Notice of Informal Patent Application 6) Other:								
Paper No(s)/Mail Date <u>8/2/05</u> . 6) Other:								

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DETAILED ACTION

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 24 is rejected under 35 U.S.C. 103(a) as being unpatentable over MIZUTA 5,828,892 in view of NELSON EP1107437A2.

Regarding claim 24.

MIZUTA discloses a method of providing a supply voltage comprising: providing a plurality of source input voltages; monitoring the source voltage; generating a control signal responsive to the source voltage being above a given level; a response to the control signal, providing a working voltage from the source voltage (col.2, lines 1-12); converting (23) the working voltage to a supply voltage.

MIZUTA does not disclose ORing the source input voltages providing a source voltage.

NELSON teaches an output power bus defined by the first diode output coupled to the second diode output is provided (providing the ORing function of to source inputs).

It would have been obvious to one having ordinary skill in the art at the time of this invention to have modified the power supply control circuit to include a first diode and a second diode to accept power from independent power sources to supply power Application/Control Number: 10/773,008

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to the output power bus upon the loss of the first isolated power source or the loss of the second isolated power source (NELSON, abstract).

Allowable Subject Matter

Claims 1-23,25 allowed.

The following is a statement of reasons for the indication of allowable subject matter: the prior art of record does not disclose or suggest, inter alia", the standy converter coupled to the OR circuit output that provides a voltage, to the control circuit, responsive to a sufficient source voltage at the OR circuit output.

Any inquiry concerning this communication should be directed to Robert L.

DeBeradinis whose number is (571) 272-2049. The Examiner can normally be reached Monday-Friday from 8:30 am to 5:00 pm.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's supervisor, Brian Sircus, can be reached on (571) 272-2058. The Fax phone number for this Group is (571) 273 8300

ROBERT L. DEBERADINIS

OCTOBER 17, 2006

ROBERT L. DEBERADINIS PRIMARY EXAMINER